uMECH Remote Control Module:

V2.00 5-24-04

base3.s01

2: Display drivers / interface

3: USB to Async Serial

TODO:

measure USB current during various phases.

UD1
ATMEGA8-TQPF

OPTENC.B	1	PD3/INT1	PD2/INT0	32	OPTENC.A
SW.C0	2	PD4/XCK/T0	PD1/TXD	31	TX
GND	3	GND	PD0/RXD	30	RX
VCC.C	4	VCC	PC6/ RESET	29	/RESET
GND	5	GND	PC5/ADC5/SCL	28	SCL
VCC.C	6	VCC	PC4/ADC4/SDA	27	SDA
/RTS	7	X1/TS1/PB6	PC3/ADC3	26	FET.COM1
/CTS	8	X2/TS2/PB7	PC2/ADC2	25	FET.COM0
SW.C1	9	PD5/T1	PC1/ADC1	24	/PWRENB
SW.C2	10	PD6/AIN0	PC0/ADC0	23	/SLEEP
SW.C3	11	PD7/AIN1	ADC7	22	USB.O1
SW.R0	12	PB0/ICP	GND	21	GND
SW.R1	13	PB1/OC1A	AREF	20	VCC.C
SW.R2	14	PB2/ OC1 B/ SS	ADC6	19	USB.O2
MOSI	15	MB3/MOSI/OC2	AVCC	18	VCC
MISO	16	PB4/MISO	PB5/SCK	17	SCK

VAL

Also drive low to enable LED drivers

Also the RCLK for the TPIC6C

Also SW.R3

select source of CPU power.

JP1

VCC.A	1	2	VCC.C
VCC	3		

VCC.A is disabled during USB suspend.

VCC comes from USB cable

Module PROM

UX1
24AA64

GND	1	A0	VCC	8	VCC.A
GND	2	A1	WP	7	GND
GND	3	A2	SCL	6	SCL
GND	4	GND	SDA	5	SDA

24LC64-i/sn
so8

CAP0805-0.1U

GND — VCC.A

AVR PROGRAMMER HEADER

PD1

VCC.C	1	VCC	RES	2	/RESET
SCL	3	IO	SCK	4	SCK
MOSI	5	MOSI	MISO	6	MISO
RX	7	RX	IO	8	SDA
TX	9	TX	GND	10	GND

HEADER_AVR11

Optical Encoder Interface

PO1

OPTENC.B	1	A1
VCC.B	2	A2
OPTENC.A	3	A3
	4	A4
GND	5	A5

SIP5

CAP0805-0.1U

GND — VCC.B

GND — testpoint TP1

VCC.C

CD1 0.1 — CD2 0.1 — CD3 10u, 6.3v

GND

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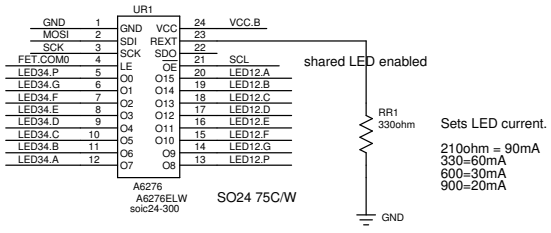
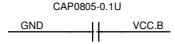
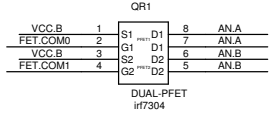
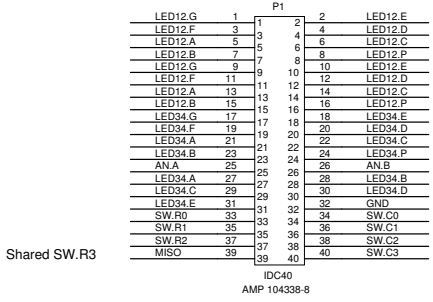
C Tue Jul 13 2004 08:41:42 D BASE3.S01









Shared SW.R3

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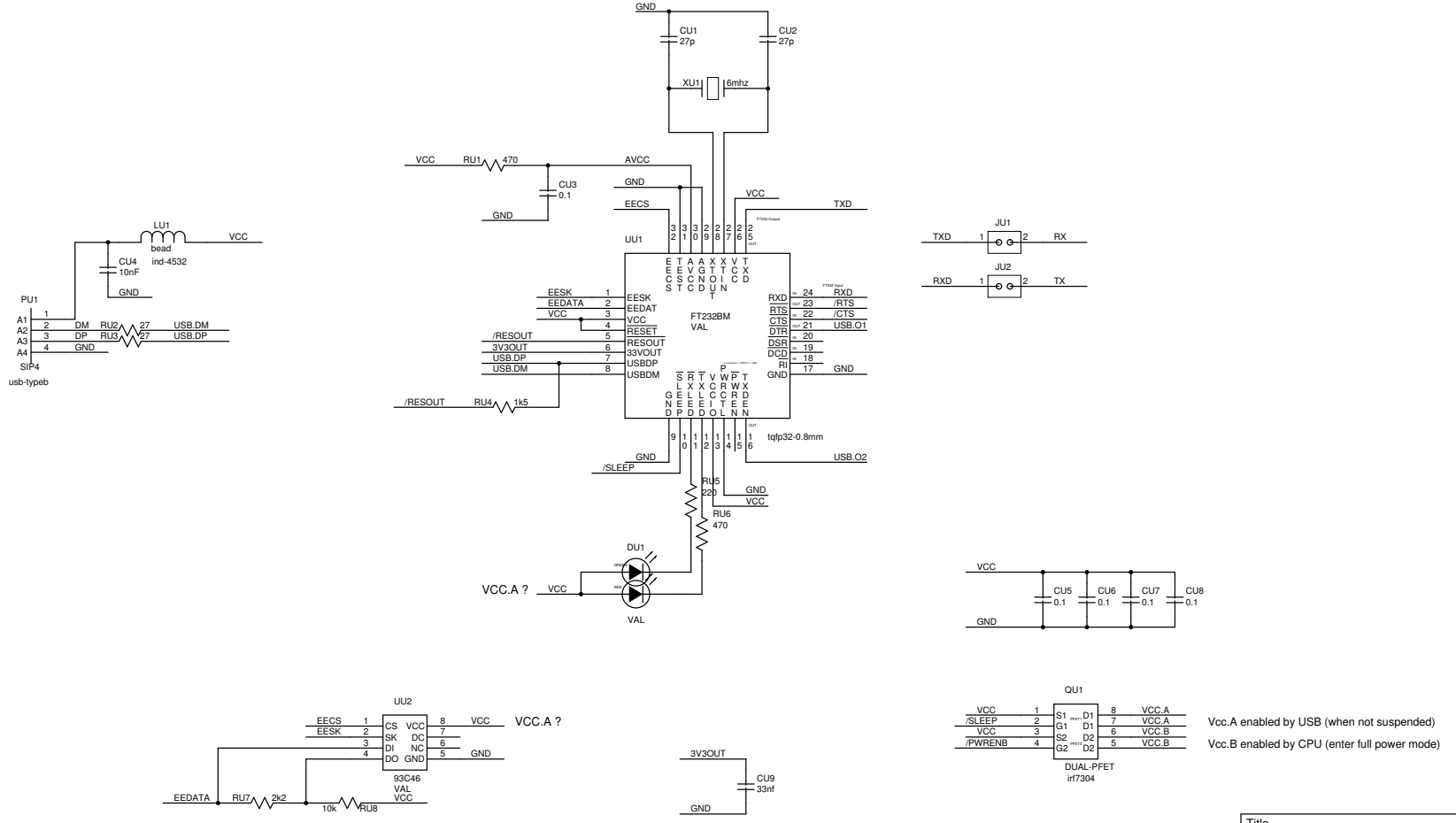

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